



UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **OKAJIMA, Yoshinori**

Group Art Unit: **2816**

Serial No.: **10/708,145**

Examiner: **Cassandra F. COX**

Filed: **February 11, 2004**

P.T.O. Confirmation No.: 2144

**FOR: TIMING CONTROLLER AND CONTROLLED DELAY CIRCUIT FOR
CONTROLLING TIMING OR DELAY TIME OF A SIGNAL BY CHANGING PHASE
THEREOF**

INFORMATION DISCLOSURE STATEMENT
PURSUANT TO 37 CFR 1.97(b)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

February 12, 2004

Sir:

The attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached Form PTO-1449. One copy of each of these documents is attached.

No fee or certification is required in connection with this Information Disclosure Statement, since it is being submitted prior to the issuance of a first official action on the merits or expiration of the three month period following the filing date or the entry of the national stage of the above-captioned application.

The above information is presented so that the Patent and Trademark Office can determine any materiality thereof to the claimed invention. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the documents cited in the attached Form PTO-1449 be made of record therein and appear on the first page of any patent to issue therefrom.

U.S. Patent Application Serial No. 10/708,145

The Commissioner is authorized to charge our Deposit Account No. 01-2340 for any fee which is deemed by the Patent and Trademark Office to be required to effect consideration of this statement.

Respectfully submitted,

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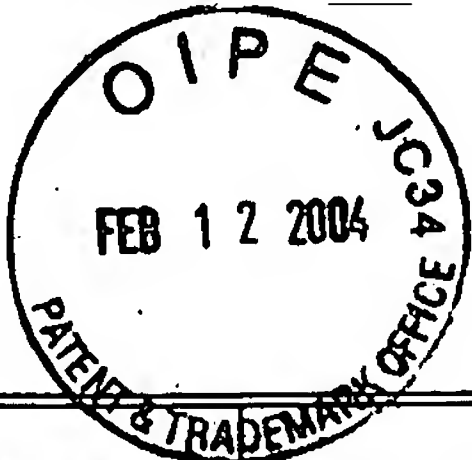
WLB/mla
Atty. Docket No. **960637D**
Suite 1000
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PATENT TRADEMARK OFFICE

Enclosures: PTO-1449



INFORMATION DISCLOSURE CITATION PTO-1449	Atty. Docket No. 960637D	Serial No. 10/708,145
	Applicant(s): OKAJIMA, Yoshinori	
	Filing Date: February 11, 2004	Group Art Unit: 2816

U.S. PATENT DOCUMENTS

Examiner Initial		Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
_____	AA						
_____	AB						
_____	AC						

FOREIGN PATENT DOCUMENTS

		Document No.	Date	Country	Translation (Yes or No)
_____	AD	4-364609	12/17/1992	Japan	No
_____	AE	55-166331	12/25/1980	Japan	Abstract
_____	AF	6-232709	08/19/1994	Japan	Abstract
_____	AG	6-112782	04/22/1994	Japan	Abstract
_____	AH	3-241918	10/29/1991	Japan	Abstract
_____	AI	0 460 669	12/11/1991	Japan	Abstract
_____	AJ				

OTHER DOCUMENTS

_____	AK	W. JUTZI, "Active Tapped Delay Line Filter", <u>IBM Technical Disclosure Bulletin</u> , Vol. 12, No. 9, February 1970.
_____	AL	
Examiner		Date Considered